

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)
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Application Number
10/500,657

Applicant(s)
Josef BERWANGER et al.

Filing Date
02-14-2005

Group Art Unit

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
/A.S./		6,011,801	01-04-2000	SOLOMON			
/A.S./		6,138,200	10-24-2000	OGILVIE			

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
/A.S./		WO 99/55036	10-28-1999	PCT			✓	
↓		WO 93/25017	12-09-1993	PCT			✓	
↓		WO 98/00941	01-08-1998	PCT			✓	
/A.S./		DE 197 06 081 A1	08-20-1998	Germany				✓

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		K. Etschberger et al., Buscontrollerbaustein für echtzeitfähige Netze, Electronik 25, August 12, 1989, Pages 79-83

EXAMINER /Alex Skripnikov/	DATE CONSIDERED 10/17/2007
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